

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

At column 10, line 21 (claim 15, line 5), cancel the words "a data transfer unit configured to" so that claim 15 should read as follows:

15. An apparatus, comprising:

at least one processor; and

at least one memory including computer program code, the at least one memory and the computer program code configured to, with the at least one processor, cause the apparatus at least to:

receive a transport format combination set reconfiguration message over a certain basic physical subchannel, said transport format combination set reconfiguration message indicating one transport format combination with a certain transport format combination identifier exclusively for signaling use and the transport format combination relating to exactly one active transport channel with a predetermined block size and cyclic redundancy check size, and

utilize a parameter indicating a change of a basic physical subchannel ordered by a network, and receipt of the transport format combination set reconfiguration message, when the transport format combination set reconfiguration message indicates a change in the size of transport format combination identifiers.

Signed and Sealed this
Twenty-sixth Day of July, 2011



David J. Kappos
Director of the United States Patent and Trademark Office